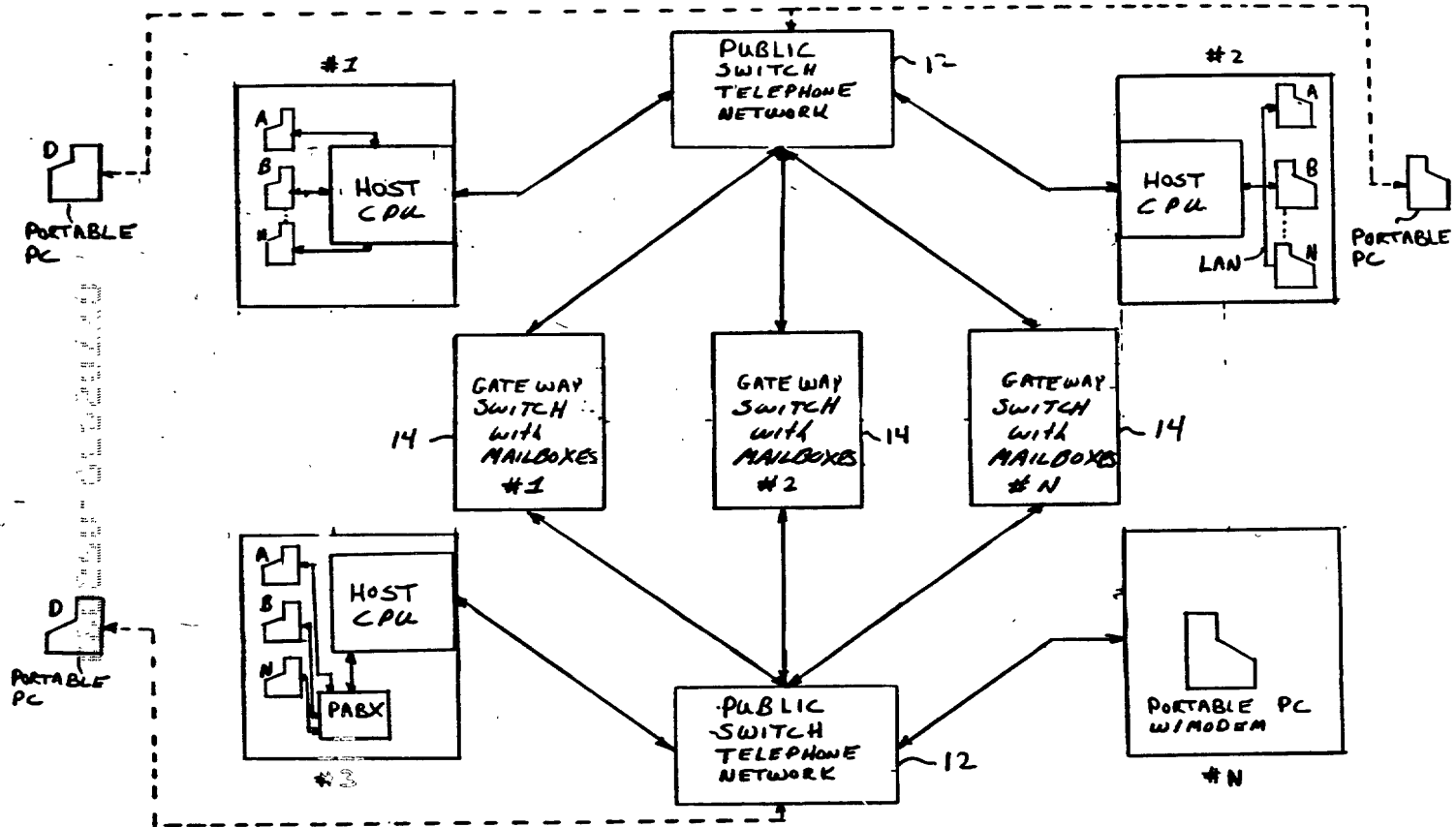


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Fig. 1  
(PRIOR ART)

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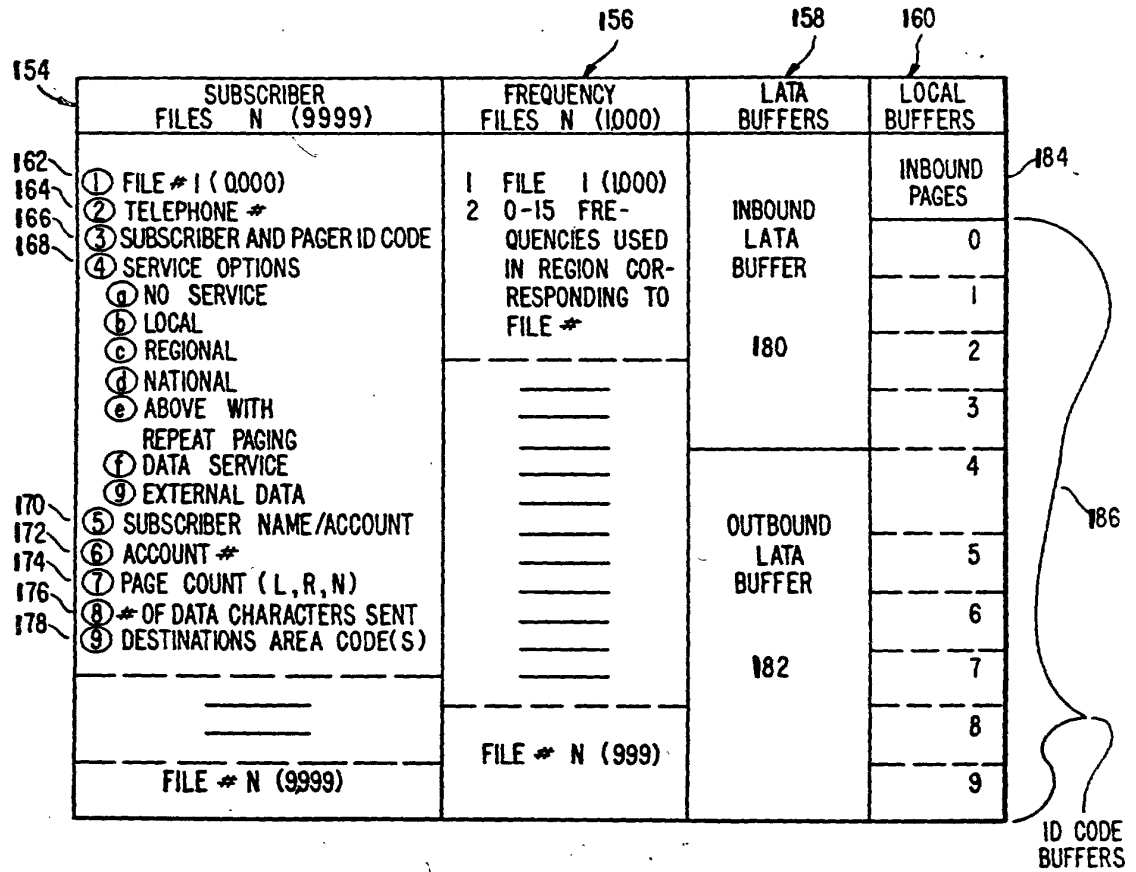




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FIG. 3  
(PRIOR ART)

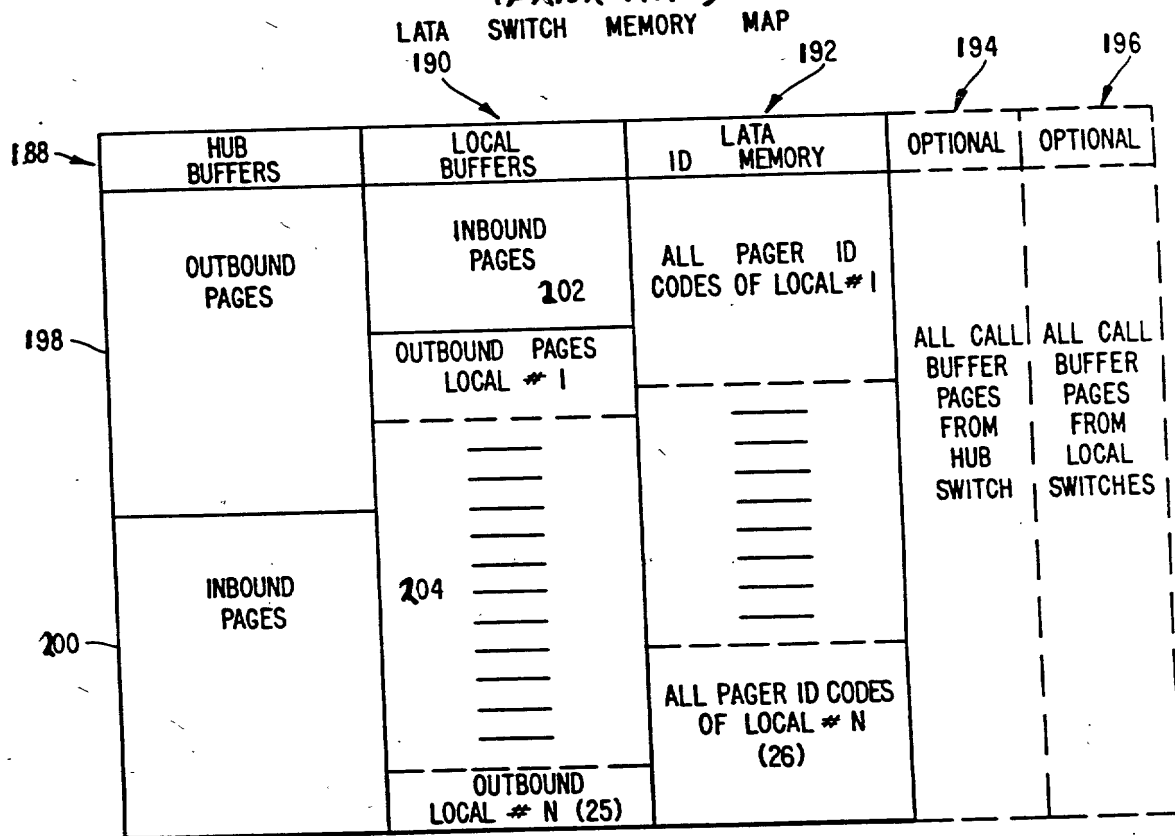
LOCAL SWITCH MEMORY MAP



3-12

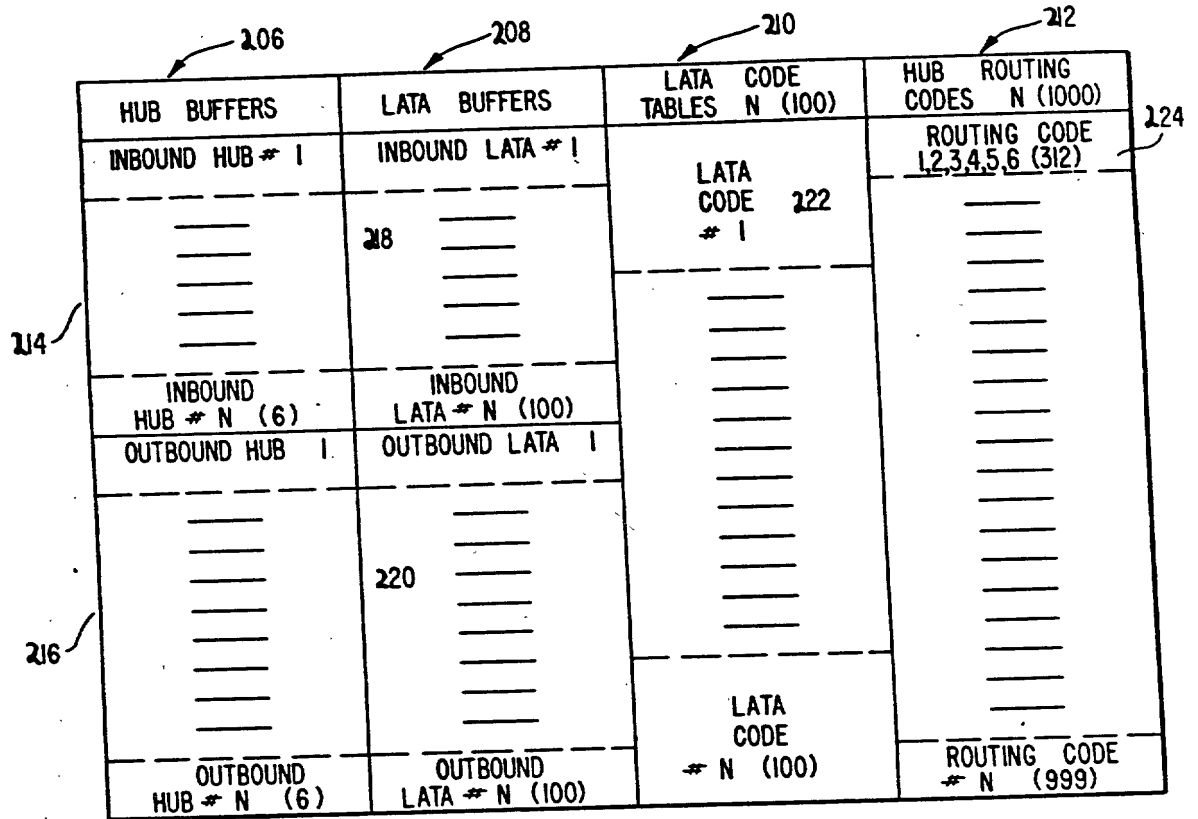
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**FIG. 4**  
**(PRIOR ART)**



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**FIG. 5**  
(PRIOR ART)  
HUB SWITCH MEMORY MAP



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**FIG. 6**  
(Prior Art)

THE FIVE LAYER MODIFIED X.25 PACKET

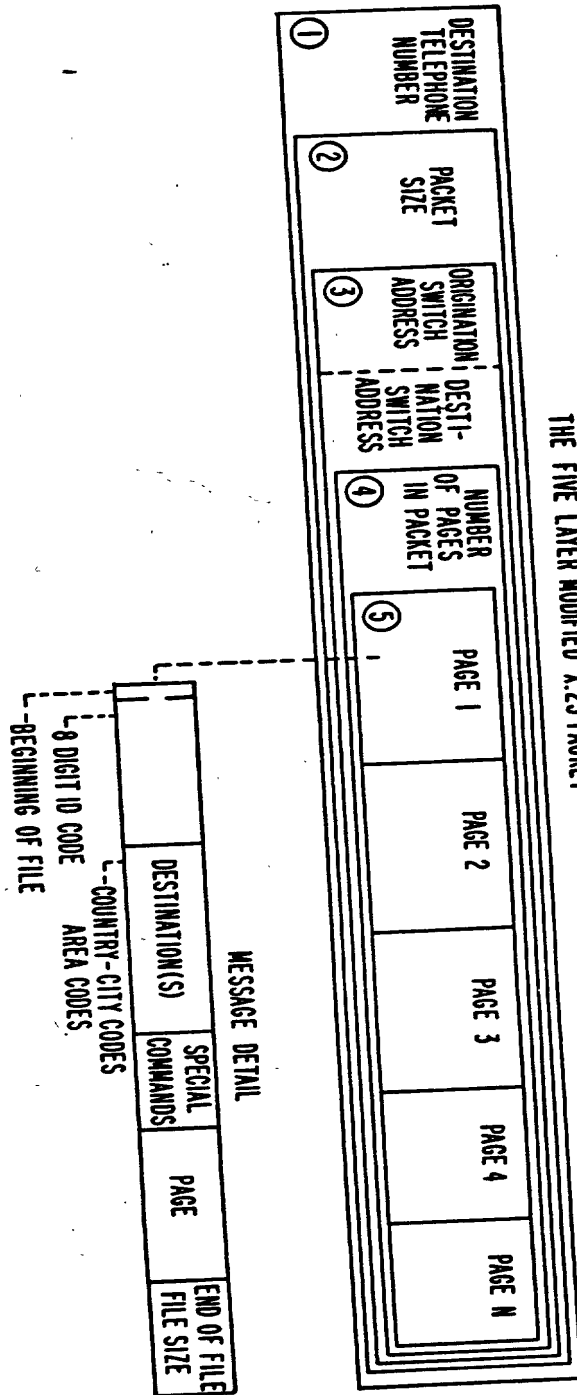
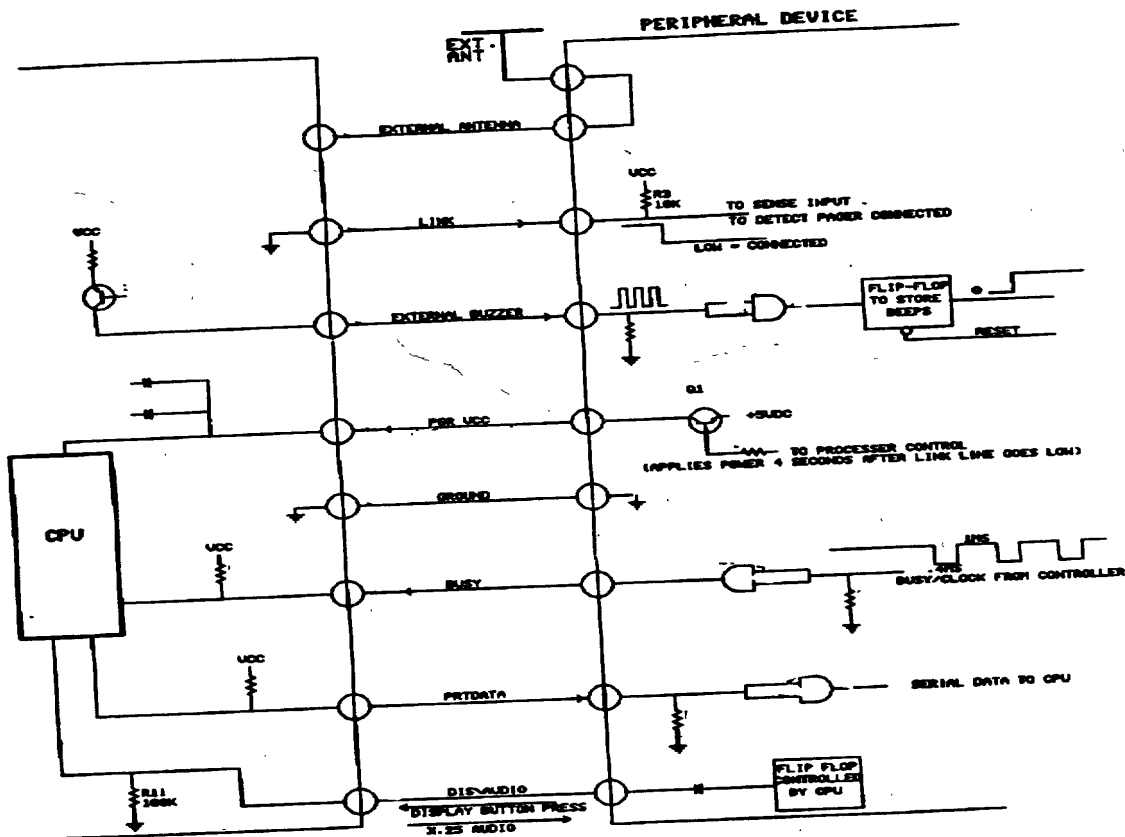


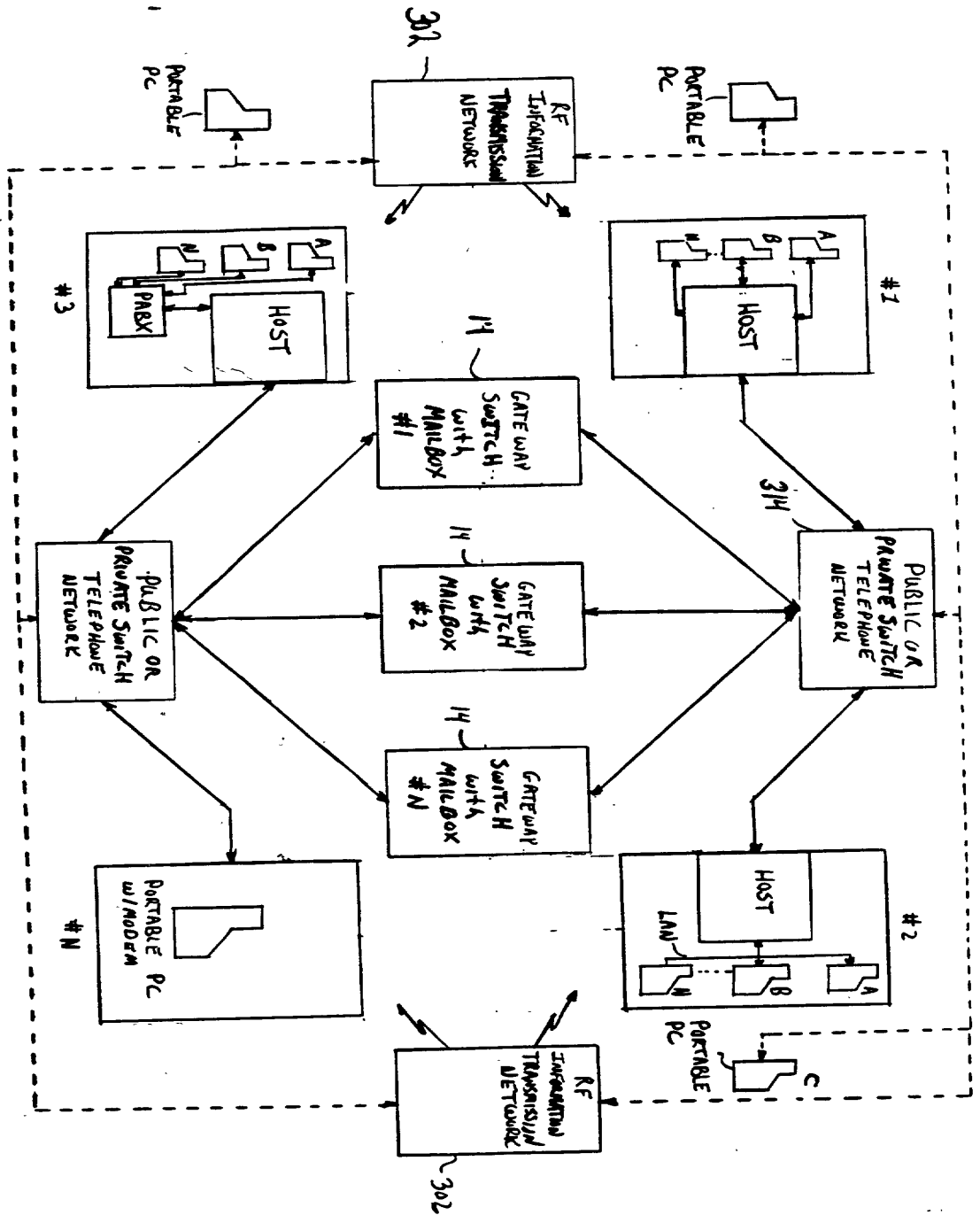
FIG. 6 is a block diagram of a five layer modified X.25 packet structure. The packet is divided into five layers. Layer 1 contains the destination telephone number. Layer 2 contains the packet size. Layer 3 contains the origination switch address. Layer 4 contains the destination switch address. Layer 5 contains the message body, which is divided into pages (PAGE 1, PAGE 2, PAGE 3, PAGE 4, and PAGE N). The number of pages in the packet is indicated. Below the packet structure, a message detail section is shown, which includes fields for destination(s), special commands, page, end of file, and file size. The message detail section is linked to the message body of the packet. Additional labels indicate: -COUNTRY-CITY CODES, -8 DIGIT ID CODE AREA CODES, and -BEGINNING OF FILE.

Fig. 7

(PRIOR ART)

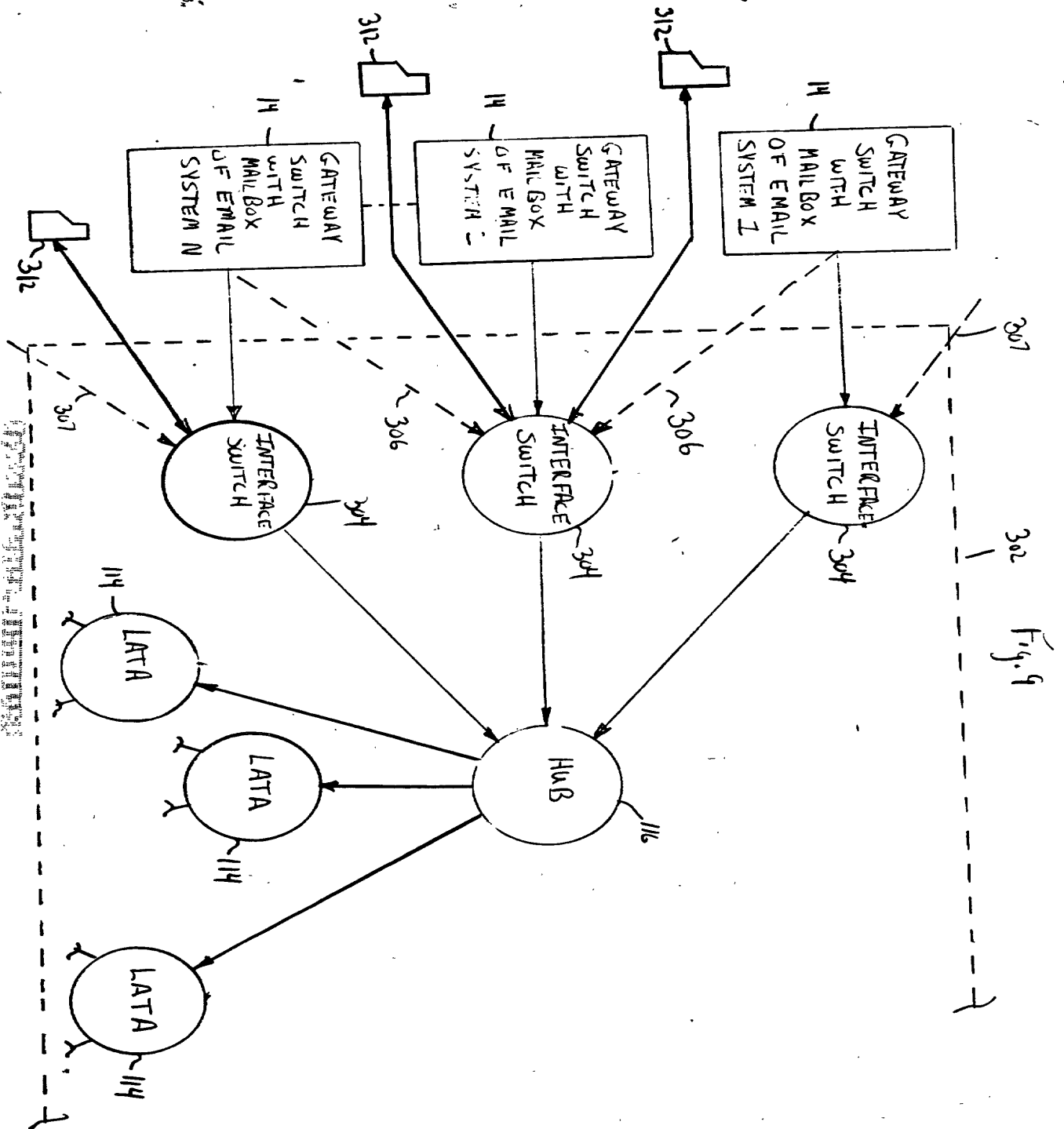


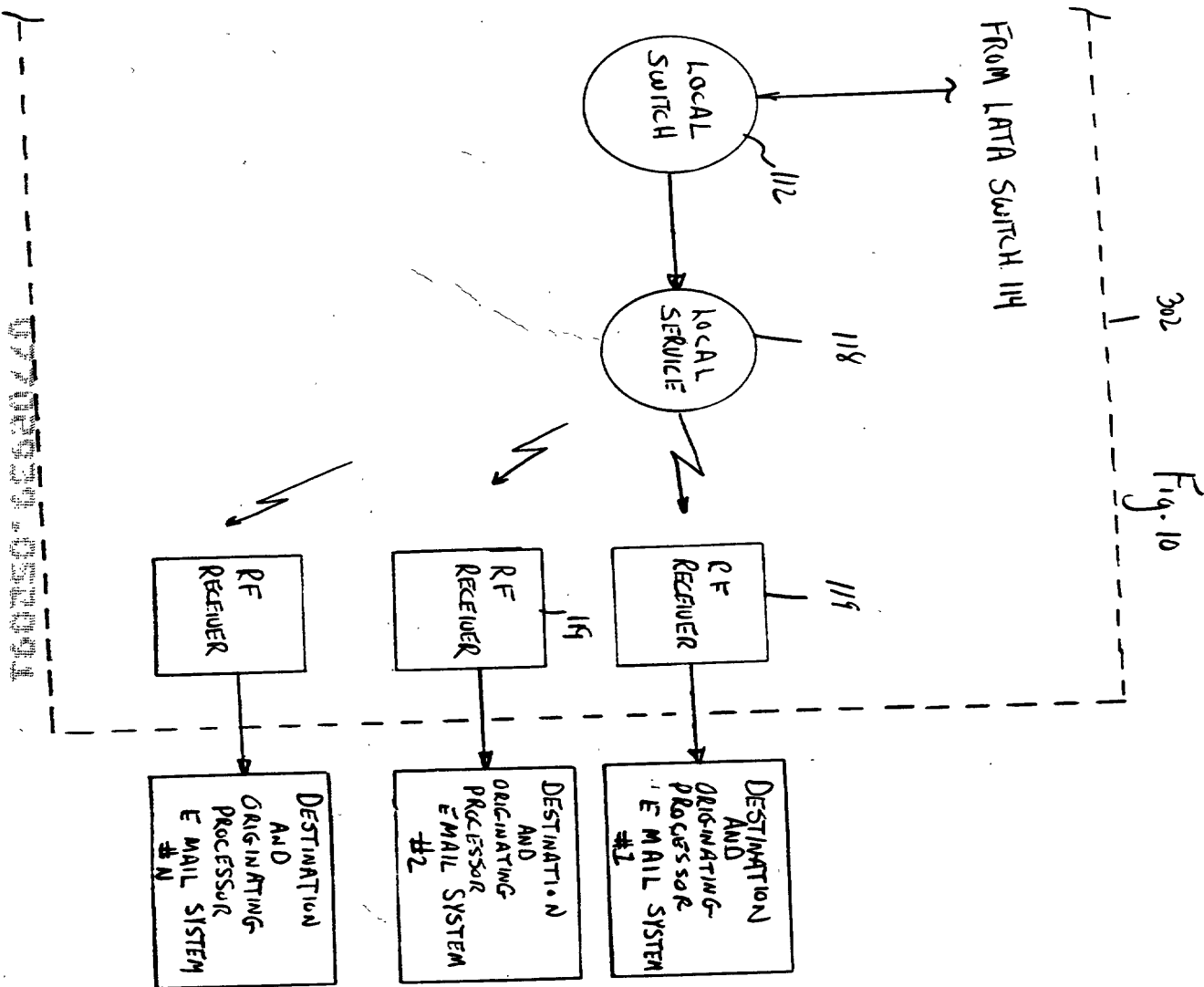
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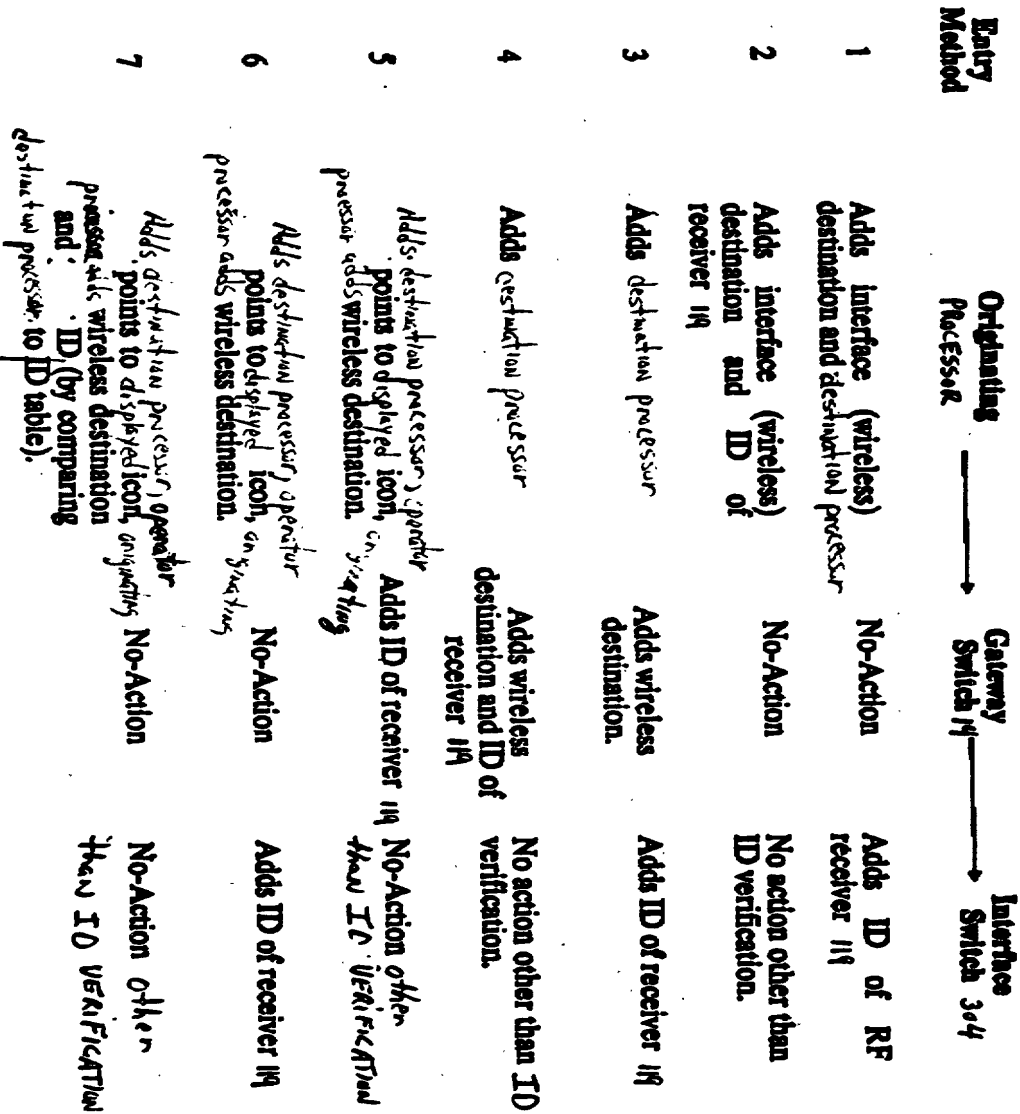




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Fig. 11

ELECTRONIC MAIL MESSAGE ENTRY METHODS



PROCESSOR 111  
SWITCH 304

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